

APPROVED O.G. FIG
BY CLASS SUBCLASS
CRAFTSMAN

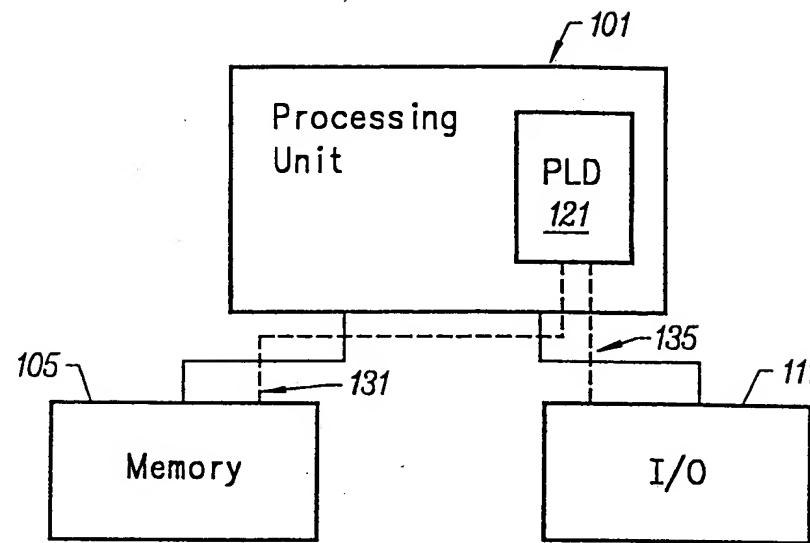


FIG. 1

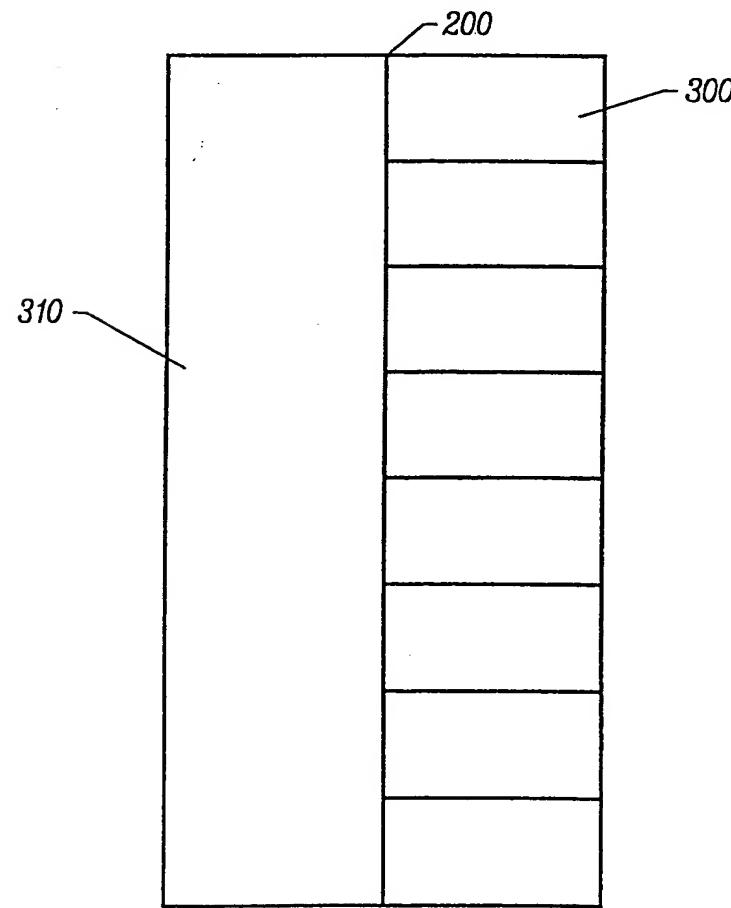


FIG. 3

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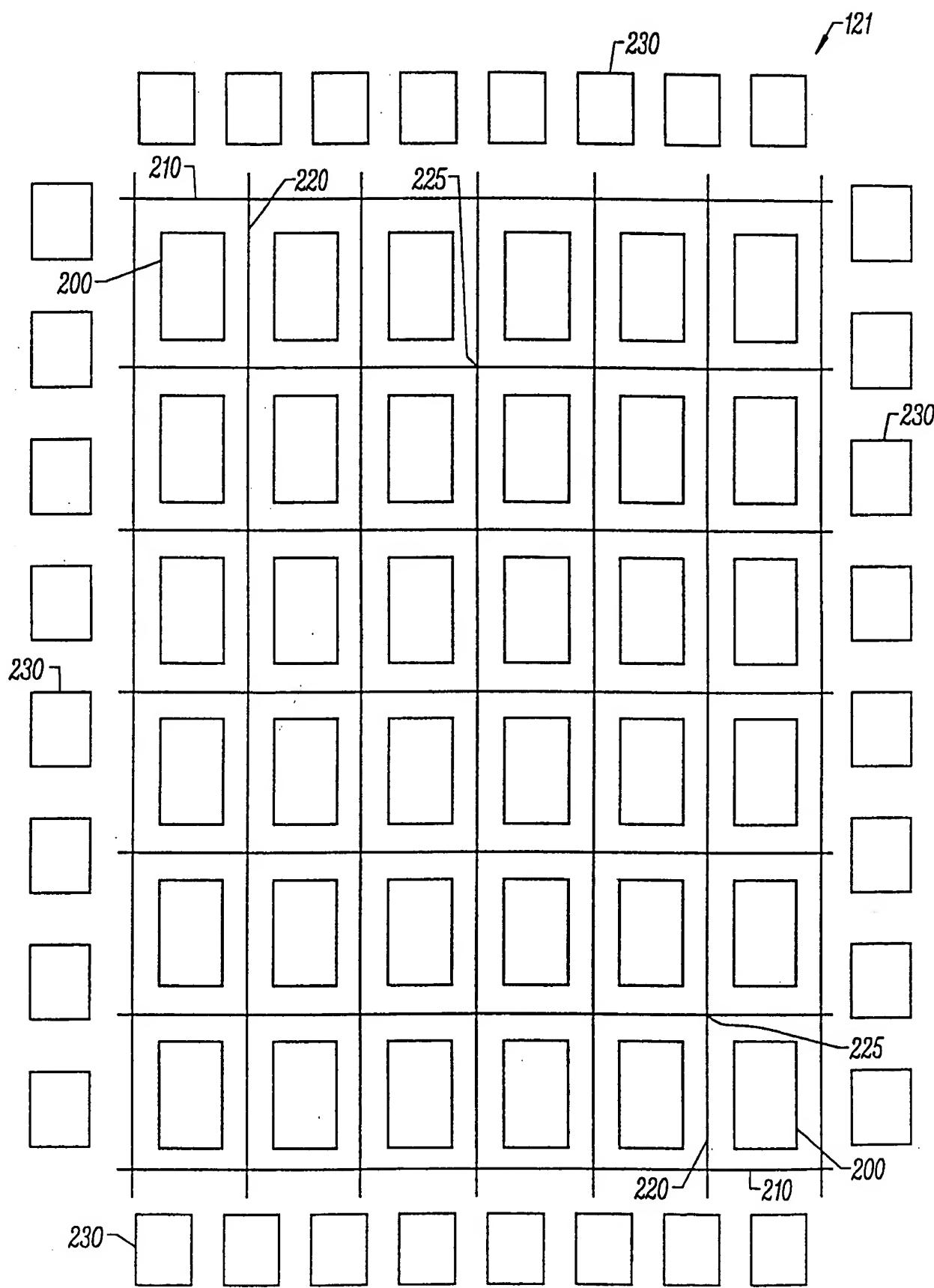


FIG. 2

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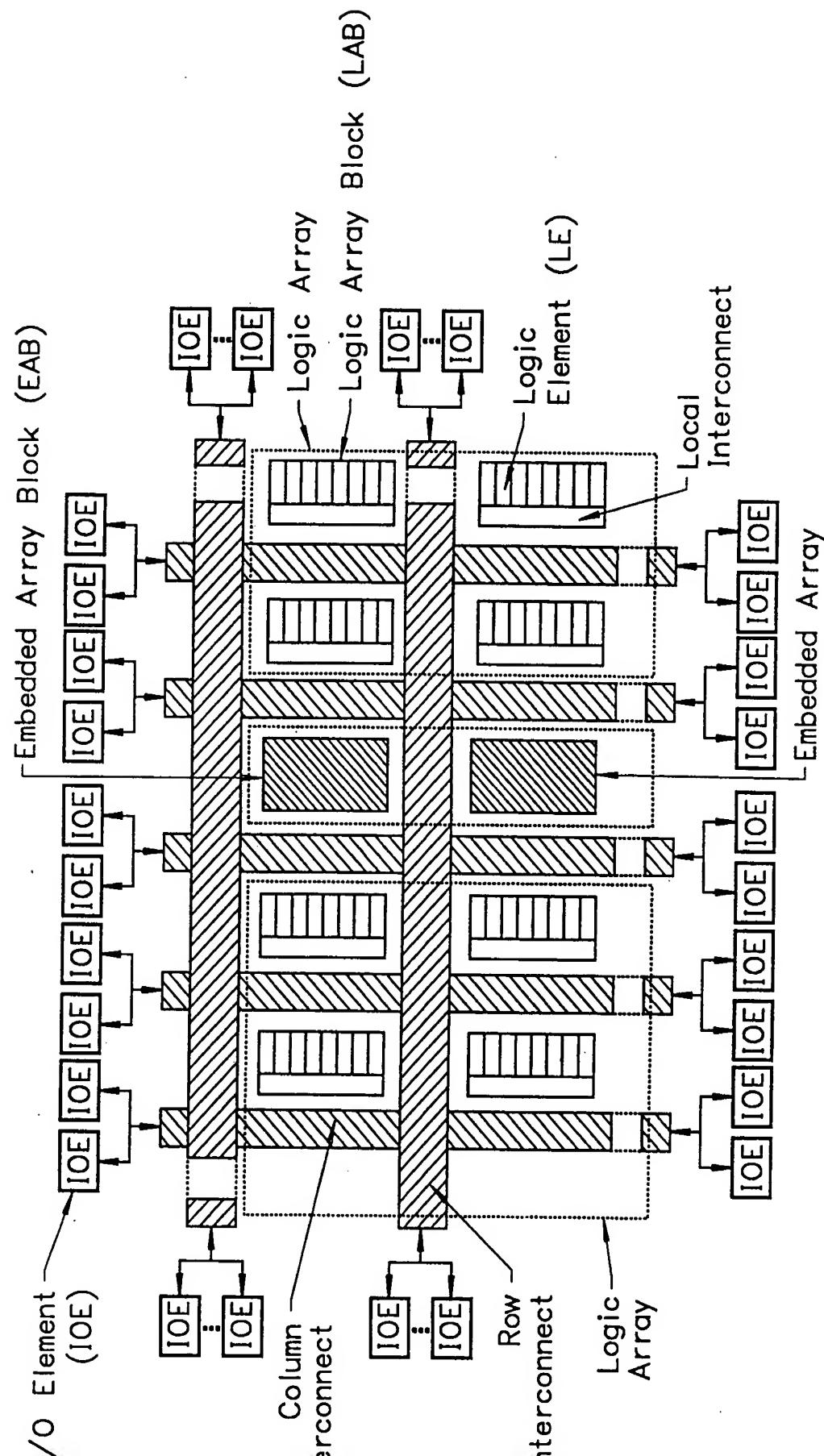


FIG. 4

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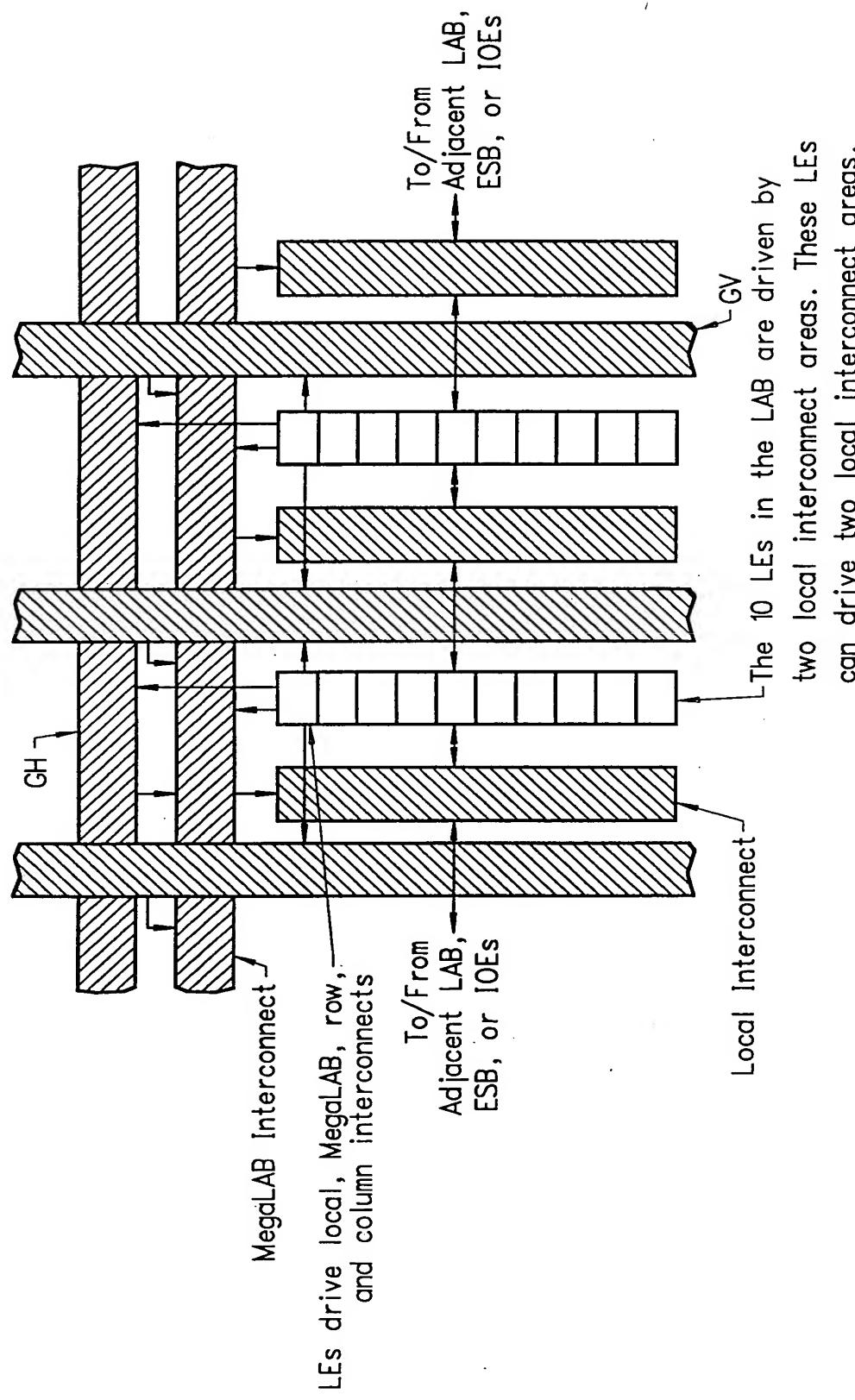


FIG. 5

The 10 LEs in the LAB are driven by two local interconnect areas. These LEs can drive two local interconnect areas.



Represents on-chip termination

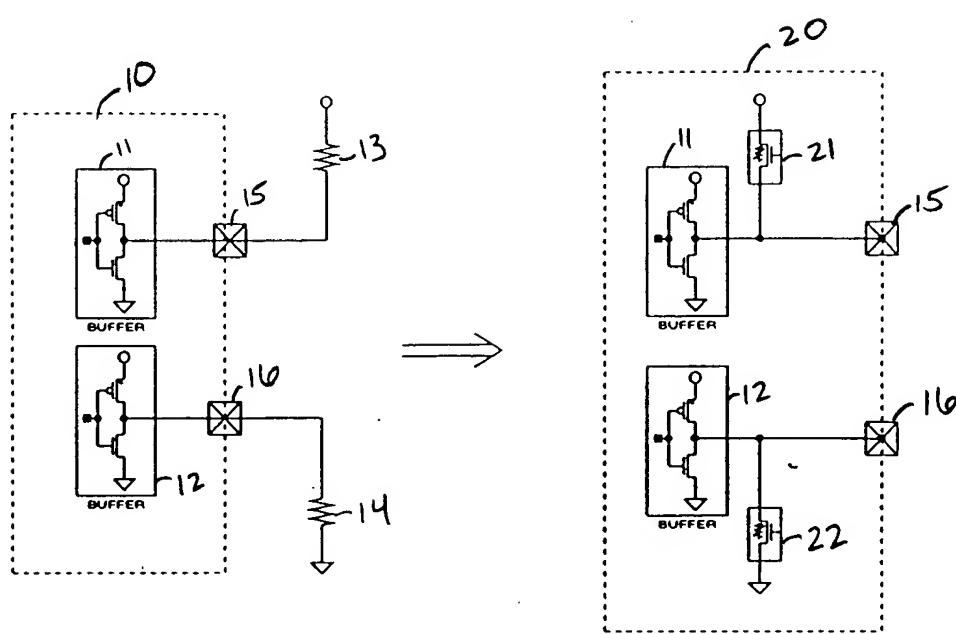


FIG. 6A

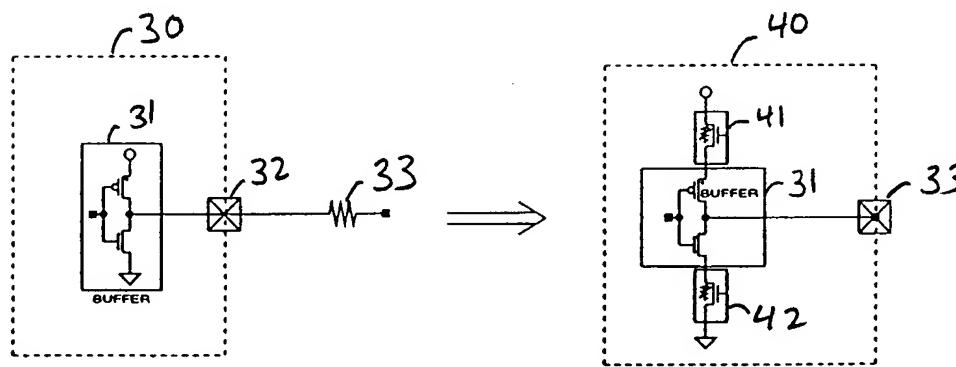


FIG. 6B

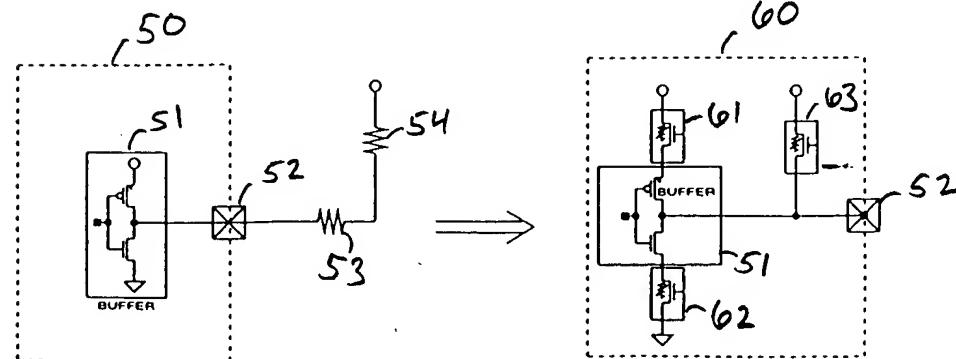


FIG. 6C

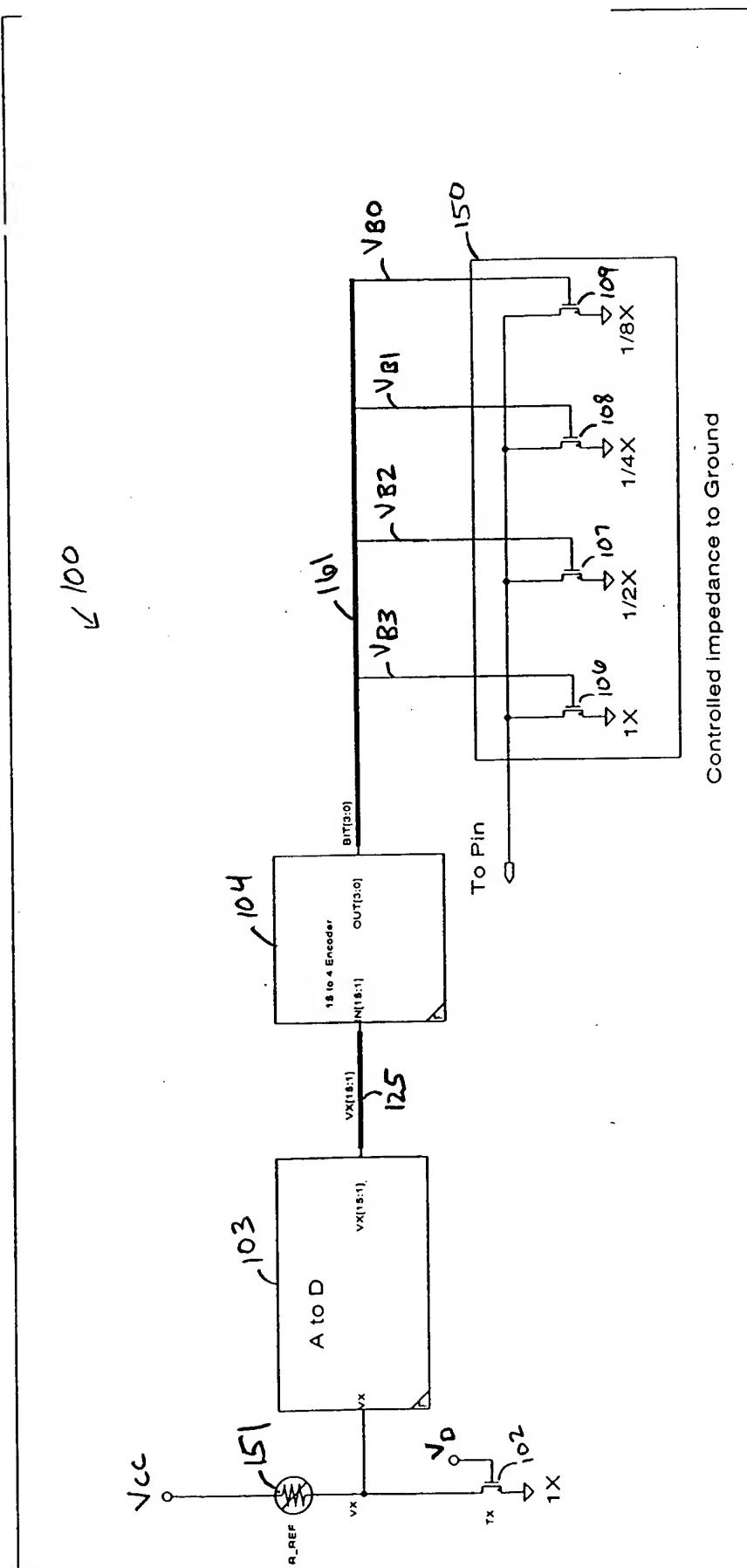
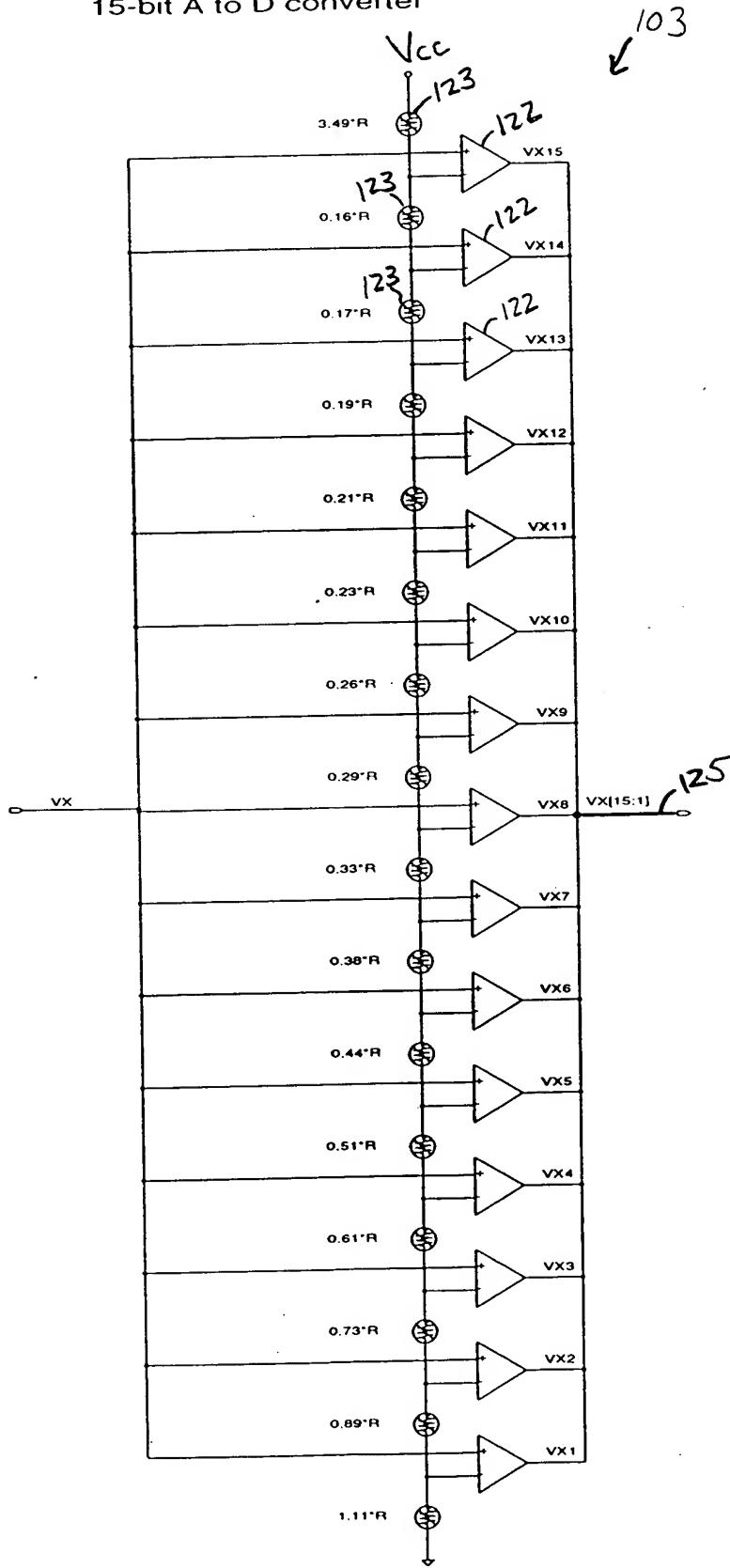


FIG. 7

Controlled Impedance to Ground

15-bit A to D converter



1000465000000000

Fig 8

10044365 • 013102

104
↙

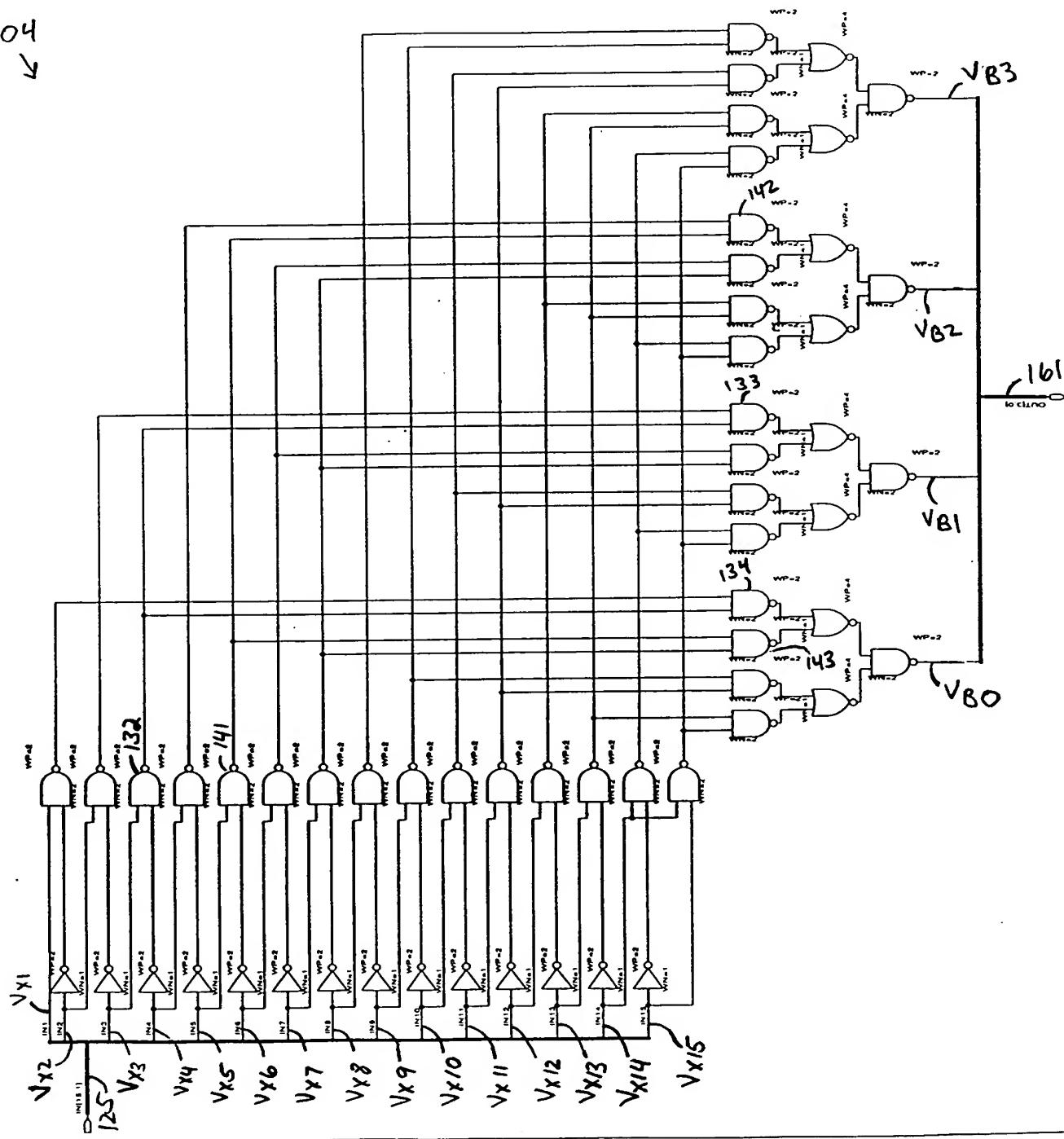


FIG. 9